

Security Standard Topology

**SW1**

Port 7	IPER1 E1/0
Port 1	IPER2 Fa0/0
Port 3	IPER4 Fa0/0
Port 5	IPER5 Fa0/0
Port 4	ASA01 Fa0/0
Port 6	ASA01 Fa0/1
Port 8	ASA01 Fa0/2
Port 10	ASA01 Fa0/3
Port 22	IPS C&C Interface
Port 23	IPS Sensing #1
Port 9	IPEBB1 E0/0
Port 24	ACS Server
Port 13 & 14	SW2
Port 16 & 17	SW3
Port 19 & 20	SW4
G0/1	SW2
G0/2	SW3

**SW2**

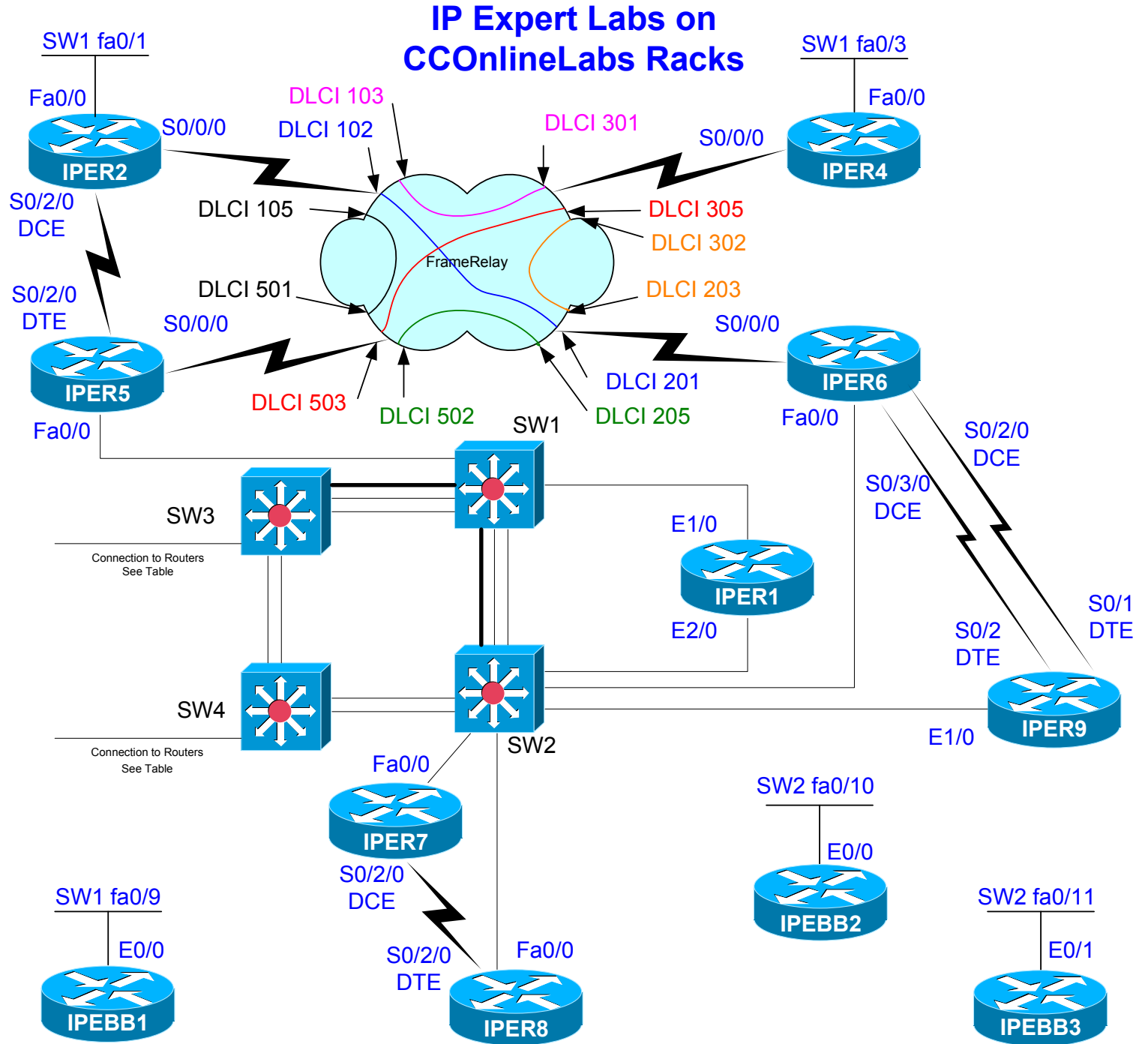
Port 12	IPER1 E2/0
Port 2	IPER6 Fa0/0
Port 4	IPER7 Fa0/0
Port 6	IPER8 Fa0/0
Port 8	IPER9 E1/0
Port 3	ASA02 Fa0/0
Port 5	ASA02 Fa0/1
Port 7	ASA02 Fa0/2
Port 9	ASA02 Fa0/3
Port 24	Test Workstation #1
Port 10	IPEBB2 E0/0
Port 11	IPEBB3 E0/1
Port 22	IPS Sensing #2
Port 13 & 14	SW1
Port 16 & 17	SW3
Port 19 & 20	SW4
G0/1	SW1

**SW3**

Port 1	IPER2 Fa0/1
Port 3	IPER4 Fa0/1
Port 5	IPER5 Fa0/1
Port 22	IPS Sensing #3
Port 13 & 14	SW1
Port 16 & 17	SW2
Port 19 & 20	SW4
G0/1	SW1

**SW4**

Port 2	IPER6 Fa0/1
Port 4	IPER7 Fa0/1
Port 6	IPER8 Fa0/1
Port 8	IPER9 E1/1
Port 22	IPS Sensing #4
Port 13 & 14	SW1
Port 16 & 17	SW2
Port 19 & 20	SW3



3.0 CCIE Blueprint

Diagram A – Base Physical Connectivity